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USA Patent Application

Dr. Frank Sattler, et al

METHOD FOR MODULATING A BASIC CLOCK  
SIGNAL FOR DIGITAL CIRCUITS AND CLOCK  
MODULATOR FOR IMPLEMENTING THE METHOD

Priority: German Patent Application  
199 33 115.4 filed July 19, 1999

Hon. Commissioner of Patents and Trademarks  
Washington, D.C. 20231

S I R :

PRELIMINARY AMENDMENT

Please amend this application simultaneously with filing as  
follows:

IN THE SPECIFICATION

PAGE 1

Line 6, delete "Description"

Line 12, before this line, after the title, insert:

~FIELD AND BACKGROUND OF THE INVENTION~

PAGE 2

Line 1, before this line insert: